

Schottky Rectifier, 100Ax2 / 100V

FEATURES

- 175°C T_J operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free
- Designed and qualified for industrial level

DESCRIPTION

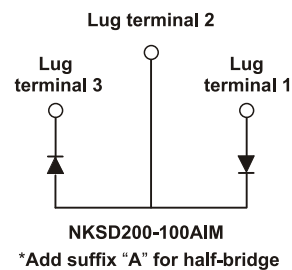
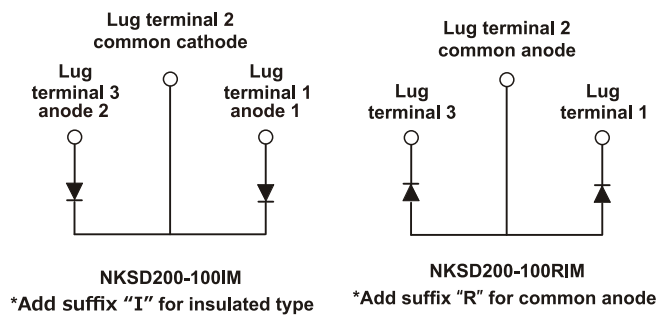
The NKSD200-100IM Schottky rectifier module series has been optimized for low reverse f leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175°C junction temperature.

TYPICAL APPLICATIONS

- High current switching power supplies
- Plating power supplies
- UPS system
- Converters
- Freewheeling
- Welder
- Reverse battery protection.



TO-244M (insulated)



PRODUCT SUMMARY

I _{F(AV)}	200A
V _R	100V

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNIT
I _{F(AV)}	Rectangular waveform	200	A
V _{RRM}		100	V
I _{FSM}	t _p = 5 μs sine	12800	A
V _F	100 Apk, T _J = 125°C (per leg)	0.70	V
T _J	Range	-55 to 175	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	VALUE	UNIT
Maximum DC reverse voltage	V _R	100	V
Maximum working peak reverse voltage	V _{RWM}		

Nell High Power Products

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNIT
Maximum average forward current per leg See fig.5	I _{F(AV)}	50% duty cycle at T _C = 135°C, rectangular waveform		100	A
				200	
Maximum peak one cycle non-repetitive surge current per leg See fig.7	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	12800	
		10 ms sine or 6 ms rect. pulse		1700	
Non- repetitive avalanche energy per leg	E _{AS}	T _J =25°C, I _{AS} =13A, L=0.2mH		15	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T _J maximum V _A =1.5xV _R typical		1	A

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNIT
Maximum forward voltage drop per leg See fig.1	V _{FM} ⁽¹⁾	100A	T _J = 25°C	0.85	V
		200A		1.05	
		100A	T _J = 125°C	0.70	
		200A		0.85	
Maximum reverse leakage current per leg See fig.2	I _{RM} ⁽¹⁾	T _J = 25°C	V _R = Rated V _R	50	μA
		T _J = 125°C		20	mA
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25°C		2650	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		7	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10000	V/μs
Maximum RMS insulation voltage	V _{INS}			1000 (1min)	V

Note

(1) Pulse width < 300 μs , duty cycle < 2%

THERMAL-MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Maximum junction and storage temperature range	T _J , T _{Stg}	-55	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}	-	-	0.50	°C/W
Thermal resistance, junction to case per module	R _{thJC}	-	-	0.25	
Thermal resistance, case to heatsink	R _{thCS}	-	0.10	-	
Weight		-	95 (3.35)	-	g(oz.)
Mounting torque, M6		30 (3.4)	-	40 (4.6)	lbf • in (N•m)
Terminal torque, M6		30 (3.4)	-	40 (4.6)	
vertical pull		-	-	80	lbf • in
2" lever pull		-	-	35	
Case style	JEDEC	TO-244AA compatible			

Ordering Information Table

Device code	NK	S	D	200	—	100	R	I	M
	①	②	③	④		⑤	⑥	⑦	⑧
①	- NPS power module								
②	- S for Schottky Barrier Diode								
③	- D for Dual Diodes, TO-244 Package								
④	- Maximum average forward current, A								
⑤	- Voltage rating (100 = 100V)								
⑥	- None for common cathode configuration "R" for common anode configuration, "A" for half-bridge configuration								
⑦	- "I" for insulated type								
⑧	- "M" for TO-224M Package (Molding type TO-244)								

Fig.1 Maximum forward voltage drop characteristics (Per Leg)

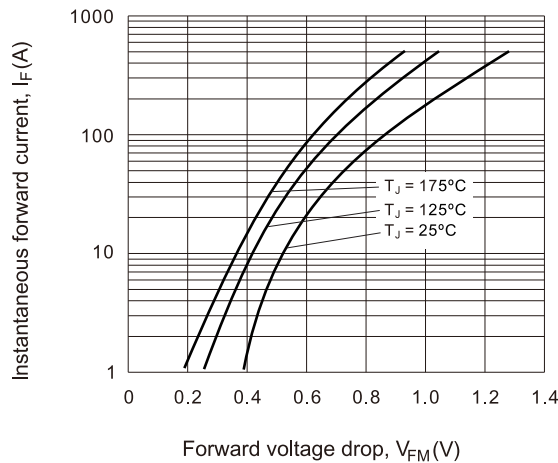


Fig.2 Typical values of reverse current vs. Reverse voltage (Per Leg)

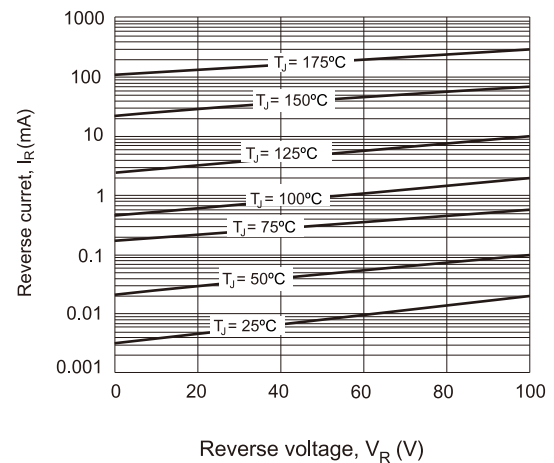


Fig.3 Maximum thermal impedance $R_{th(j-c)}$ characteristics (Per Leg)

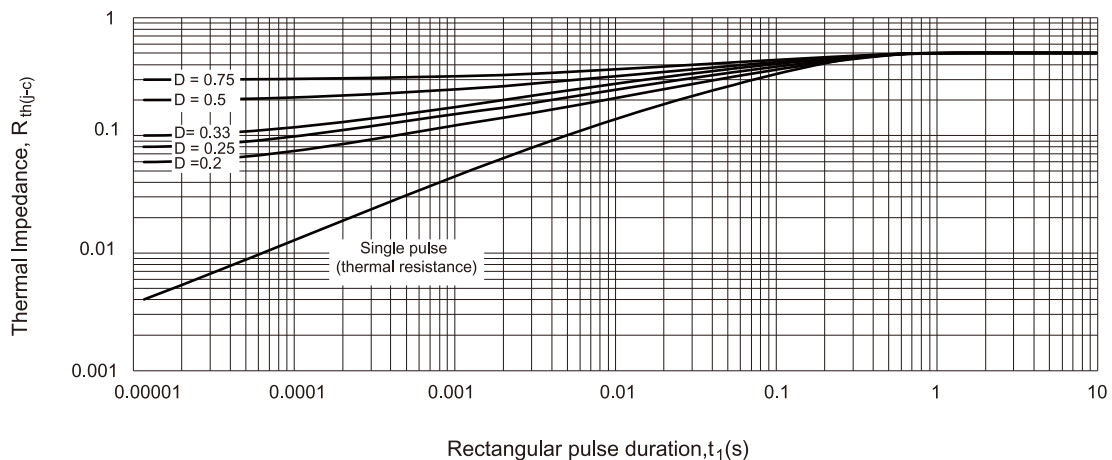


Fig.4 Typical junction capacitance vs. Reverse voltage (Per Leg)

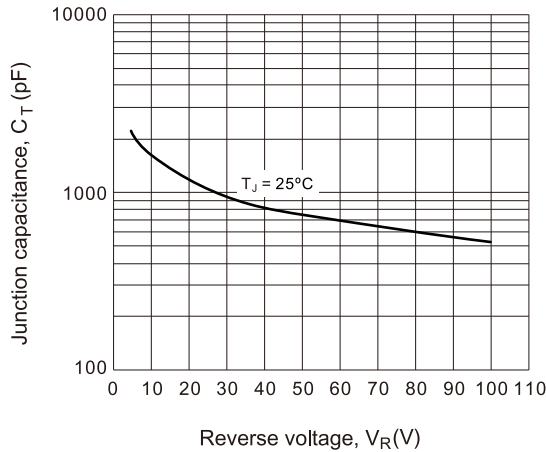


Fig.5 Maximum allowable case temperature vs. Average forward current (Per Leg)

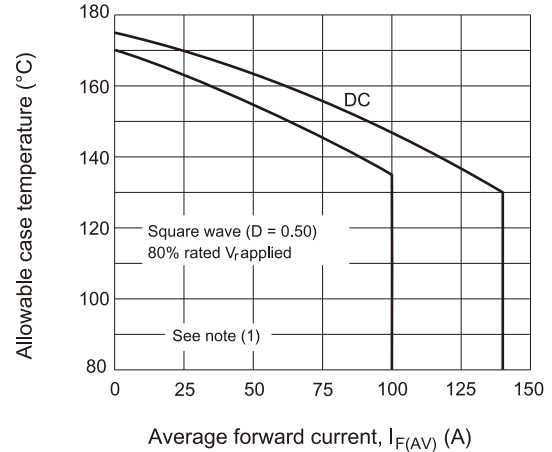


Fig.6 Forward power loss characteristics (Per Leg)

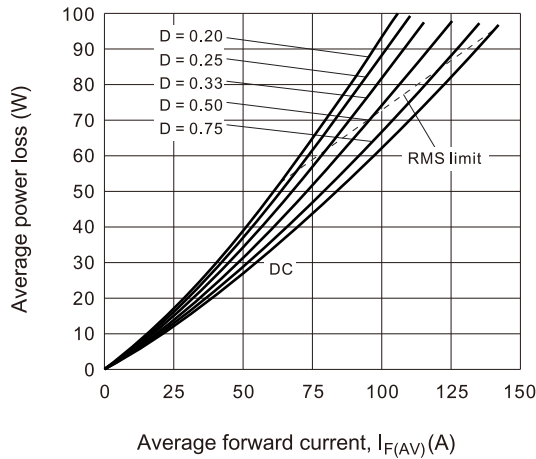


Fig.7 Maximum non-repetitive surge current (Per Leg)

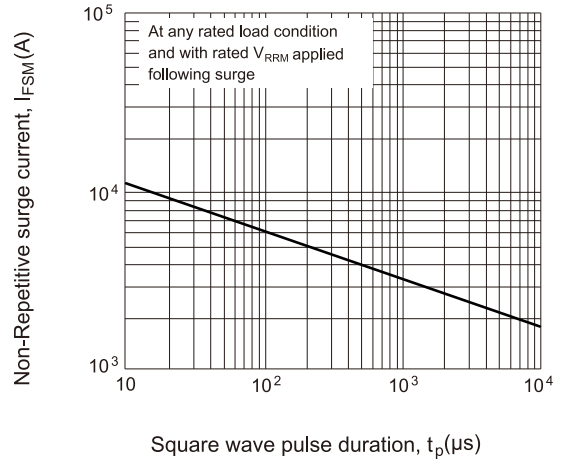
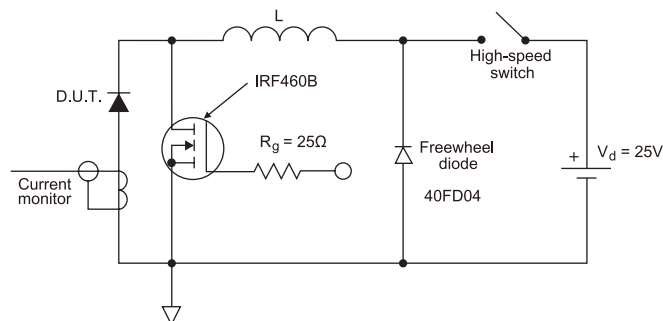


Fig.8 Unclamped Inductive test circuit



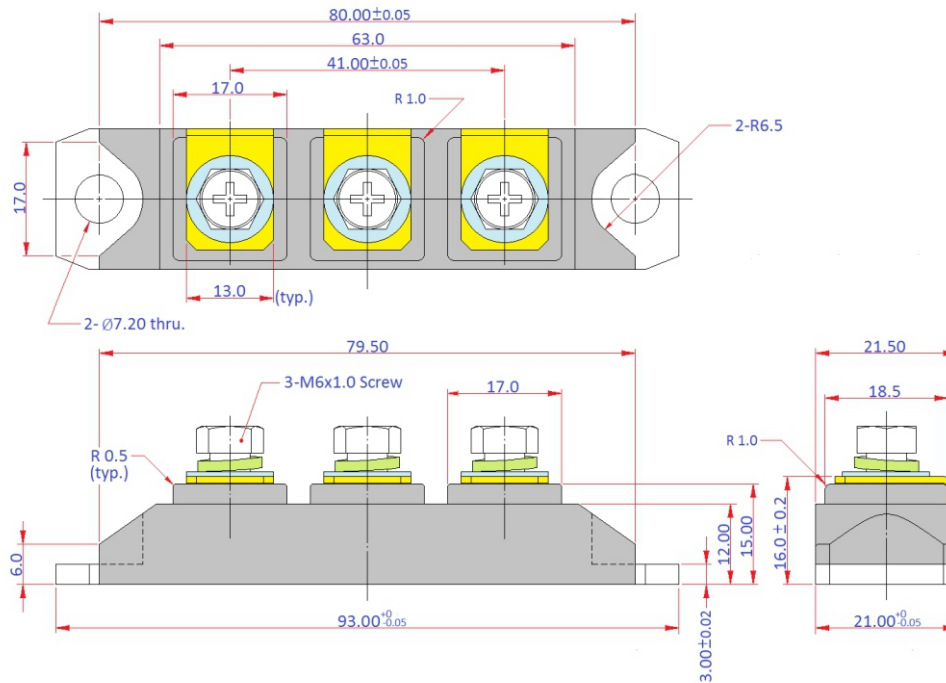
Note

(1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;

P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig.6)

P_{dREV} = Inverse power loss = $V_{R1} \times I_{R1}(1-D)$; I_{R1} at $V_{R1} = 80\%$ rated V_R

TO-244M (Insulated)



All dimensions in millimeters